

Embedded Software Engineer:

The team does all the SW for the company's products and deals with many tasks (power conversion algorithmic, communication algorithmic, optimizations, RT problems) and platforms (DSPs, Company's ASIC, operating systems, and more).

Requirements:

University Excellent graduate, B.Sc. Electrical Engineering

1-2 years of experience in the field of Real-time, DSPs, Microcontrollers etc.

Preferably: knowing of TI's DSPs, experience in writing code for both system level and lower levels.

Hard worker; willing to invest efforts and time including long hours when needed

Please send CV to: solaredge.eengineer@applynow.io

Integration Engineer:

Conduct system and product tests

Build and update test plans, results analysis, documentation

Tight work with the different R&D groups

Requirements:

University graduate, B.Sc. Electrical Engineering

Experience in multidisciplinary company - advantage

R&D "hands-on" experience – advantage

Strong analytical skills

Excellent organizational skills

Ability to work alone (and take responsibility for tasks)

Ability to focus on a single task for a period of time

Good interaction with co-workers from all the disciplines (Service oriented)

Please send CV to: solaredge.integratione@applynow.io

Power Engineer:

The Power engineer in SolarEdge works in the R&D core teams, and leads the development of the company's flagship products.

The Power engineer is responsible for the inverters' system, from the design stage to the final product, including all tests necessary to deliver the product to mass production (electronic tests, thermal tests, EMI tests...).

The Power Engineer will need to understand the inverters' system and will work with the inverter's power engineers and mechanical engineers.

Requirements:

BSC in Electrical engineering

Knowledge/experience in power electronics - Advantage

Knowledge in Analog circuits and Op Amps behavior – Advantage.

1+years of experience in circuit design and HW board design – Advantage

Knowledge in power layout design – Advantage

Experience with lab equipment such as Spectrum, Scope, Electronic Loads act

Knowledge in inverters topologies and systems is a plus – Advantage

Please send CV to: solaredge.powerenginnermaster@applynow.io

Digital Hardware Engineer:

Job Description:

The team lead and develop the company Interfaces, RF, digital and low voltage products.

Developing from marketing specification to mass production.

Designing, simulating, developing and testing (in board/system level) and lead the improvement of products in mass production.

Tight working with different groups in the company (Marketing, Mechanics, NPI, Engineering, ATE and Reliability).

Requirements:

B.Sc. Electrical Engineering – Must

Experience in Digital/HW board design – advantage

Experience with design of DC/DC, SPI/I2C/UART, USB/WiFi/Ethernet/CDMA/GSM/ZigBee, MCU/DSPs, DDR – advantage

FPGA/CPLD design and validation – advantage

Please send CV to: solaredge.digitalengineer@applynow.io

Analog Hardware Engineer:

Job Description:

The HW engineer will take part of developing a cutting edge products for the solar market.

The engineer will take part from the design and development stages of versatile electronic circuits (both analog and digital), through board layout and testing till we have a certified product ready to mass production.

During all stages the HW engineer will work in cooperation with mechanical team, embedded and integration teams as well as other HW teams.

The HW engineer will get deep understanding in both the electrical circuits, full SolarEdge system and how mass production works.

Requirements:

BSC in Electrical engineering.

Experience in Analog Board Design.

Proven experience working with lab equipment such as Spectrum, Scope, Electronic Loads act.

Knowledge in Orcad, Visio and Pspice.

Knowledge of DC2DC converter topologies such as Buck, Boost, Flyback and forward - an advantage.



Experience with Circuit design.

Experience with Layout.

Please send CV to: solaredge.analogeng@applynow.io