Chip Design Engineer\Student

We are looking to hire a Chip Design Engineer\Student to join Mellanox' s Chip Design R&D team.

The work environment is versatile, educational, dynamic and challenging as our employees are currently working on innovative, next-generation networking devices at the forefront of technology in terms of performance and power efficiency. The daily work involves all aspects of chip development, mainly design, verification and post silicon debug. Work with the best and become one of the best!

Qualifications:
 B.SC in Computer Engineering /Electrical Engineering/Communication Engineering or Student for EE.
0-10 years of proven experience in RTL Front End Asic Design (Chip Design or verification).

Advantages:
Knowledge in HDL (Verilog/VHDL)
Knowledge in Specman.
Hands on experience in DV in all levels (BL, SL, FC)
Knowledge in Networks/CPUs/u-controllers/multi-core systems.
Hands on experience in scripting languages (Python, Perl, etc.)